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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of : Atty. Docket No. POU920010165US1
Marvin J. RICH et al. :
Patent No.: 6,817,000 *82* :
Issue Date: November 9, 2004 :
For: *DELAY CORRELATION ANALYSIS AND REPRESENTATION FOR VITAL
COMPLIANT VHDL MODELS*

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
DEC 20 2004
of Correction

Dear Sir:

Attached hereto is a Certificate of Correction to correct errors in the above-referenced Letters Patent.

All of the errors being corrected are attributable to the U.S. Patent and Trademark Office, so it is submitted that no fee is required. However, if it should be determined that a fee is required, the Commissioner is authorized to charge the fee to Deposit Account No. 09-0463.

Respectfully submitted,

Date:

12/8/04

By:

Jose Gutman
Jose Gutman
Registration No. 35,171

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,817,000 *B2*
DATED : November 9, 2004
INVENTOR(S) : Marvin J. RICH et al.

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Please make the following changes:

Column 11, line 13 from "VIIDL" to - - VHDL - -
Column 15, line 12 from "VIIDL" to - - VHDL - -
Column 17, line 25 from "mode" to - - model - -

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PATENT NO. **6,817,000**

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